

CLAIMS

- 1 1. A method for reading data in a burst from a memory to a PCI master in response to a burst read access by said PCI master, said burst read access identifying a starting address in a line Ln of said memory, in a system which includes a CPU having a first level cache, comprising the steps of:
 - 4 reading data from said memory according to said burst read access; and
 - 5 simultaneously performing an inquiry cycle of line Ln+1 in said first level cache.